

Figure 1 shows a dual output 1.8V/10A and 2.5V/2A circuit using the LTC1704. This is a typical FPGA application where 1.8V is the core voltage and 2.5V is for I/O. In this case, the switcher supplies 1.8V and the LDO supplies 2.5V, taking power from either 3.3V or 5V for the external pass transistor.

The switcher channel uses all N-channel MOSFETs for improved efficiency and lower cost. R9 and R10 program the output voltage. Type III compensation—C9, R4, C8, R8 and C13—allows maximum flexibility in the choice of LC filter components. The current limit circuit uses the $R_{DS(ON)}$ of the bottom MOSFET to sense inductor current. A 10 μ A current from the I_{MAX} pin flowing into R2 produces the reference voltage for current limit. The current limit circuit discharges the soft start capacitor C7 to control output current.

The linear regulator uses an external high gain low V_{CESAT} NPN series pass transistor Q3. The output voltage is $0.8V \cdot (1 + R6/R11)$. The maximum output voltage is limited to

$(V_{CC} - V_{DRV} - V_{BE})$ and by $(V_{C(Q5)} - V_{CESAT})$. The maximum driver voltage drop (V_{DRV}) is 1.1V at 30mA. Limiting the base drive current provides short circuit protection. R7 programs max base current drive. Pulling REGLIM down to below 0.8V turns off the LDO.

Figure 2 shows the efficiency of the 1.8V output over a 1A to 10A current range, over which the efficiency remains close to 90%. Figure 3 shows the load step response of the switcher to a 4A to 10A load step. At each edge of the load step, less than 50mV transient deviation occurs when using three 180 μ F 4V solid polymer capacitors.

Conclusion

The LTC1704 is suitable for applications requiring a high power switcher and a moderate power linear regulator where the cost and complexity of a second switcher would be unjustifiable. For applications that require more power from the 2nd output than is practical with a linear regulator, the LTC1702A is a good choice with its two switchers that can deliver up to 15A each.

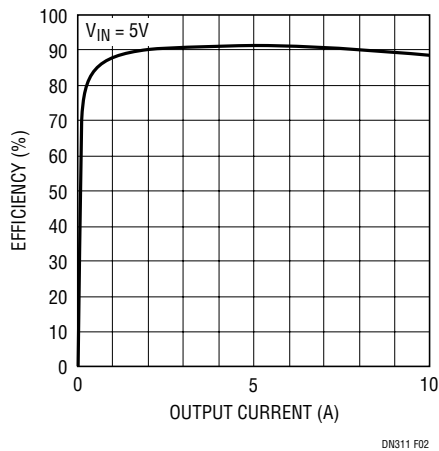


Figure 2. 1.8V Efficiency

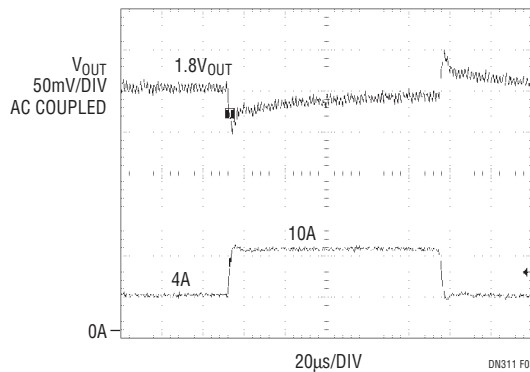


Figure 3. Step Load Response of 1.8V Output

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